



Attorney Docket No. 259/023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ji-Young KIM, et al.

Serial No. 10/759,239

Filed: January 20, 2004

For: VERTICAL DOUBLE-CHANNEL SILICON-ON-
INSULATOR TRANSISTOR AND METHOD OF
MANUFACTURING THE SAME

Notice of Allowance

Mailed: June 17, 2005

Art Unit 2814

Examiner: Long PHAM

Confirmation No. 8062

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §1.56

Mail Stop ISSUE FEE

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 (or PTO-1449, *modified*) is a listing of a document(s) known to applicant in order to comply with applicant's duty of disclosure pursuant to 37 C.F.R. §1.56.

☐ A copy of each listed document is being submitted to comply with the provisions of 37 C.F.R. §§1.97 & 1.98. [Box checked if applicable.]

☒ A copy of each listed non-U.S. patent and/or patent publication and cited literature is being submitted to comply with the provisions of 37 C.F.R. §§1.97 & 1.98. Cited U.S. patents and patent publications are *not* attached in accordance with the waiver set forth in the Notice by the Deputy Commissioner for Patent Examination Policy dated July 11, 2003, applicable to applications having a U.S. filing date *after* June 30, 2003. [Box checked if applicable.]

* * *

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present

application. Applicant does not waive any rights to take any action that would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* prior art reference against the claims of the present application.

* * *

☐ The listed document(s) is/are being submitted in compliance with 37 C.F.R. 1.97(b)(3): submission before the mailing date of the first action on the merits. [Box checked if applicable.]

☒ Citation number 1 is a correction of a typographical error in the IDS originally filed January 20, 2004. The correct number of the OH patent issued August 6, 2002, is U.S. 6,429,055 B2.

☒ Citations numbered 2-6 came to applicant's attention during the prosecution of their corresponding UK application.


☒ A copy of the UK Search Report is also attached. It is noted that in said UK Search Report each of the cited references is designated a Category "A" reference: "Document indicating technological background and/or state of the art." [Box(es) checked if applicable.]

* * *

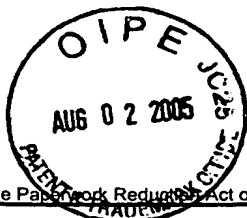
It is requested that this Statement and the references be placed in the official file of the captioned application.

Respectfully submitted,

August 2, 2005
Date


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PTO/SB/08B (04-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/759,239
Filing Date	January 20, 2004
First Named Inventor	Ji-Young KIM, et al.
Art Unit	2814
Examiner Name	Long PHAM
Attorney Docket Number	259/023

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	6	CHOI, et al., "A Spacer Patterning Technology for Nanoscale CMOS",	
		IEEE Transactionson Electron Devices, Vol. 49, No. 3, pp. 436-441 (March 2002)	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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